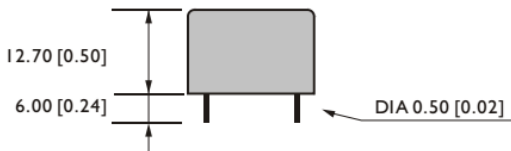
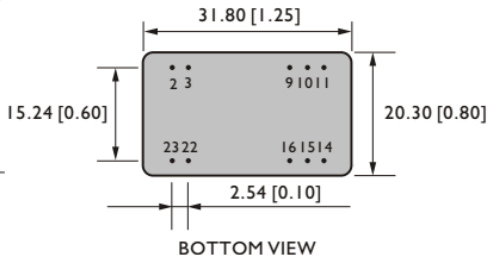


# MECHANISM & PIN CONFIGURATION

mm [inch]



GENERAL TOLERANCE	
0.00[0.00] - 30.00[1.18]	±0.30[0.01]
30.00[1.18] - 120.00[4.72]	±0.50[0.02]



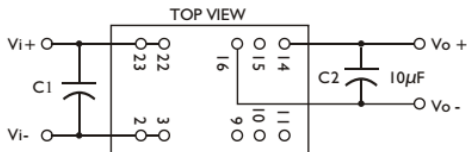
## PIN ASSIGNMENT

### GENERAL

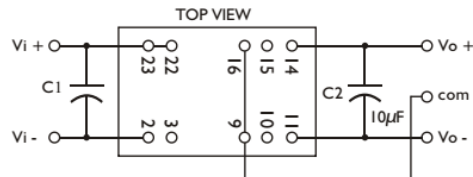
PIN NO.	2&3	9	10&15	11	14	16	22&23
SINGLE	Vi -	N. C.	N. C.	N. C.	Vo +	Vo -	Vi +
DUAL	Vi -	com	N. C.	Vo -	Vo +	com	Vi +

## APPLICATION CIRCUIT

a. SINGLE OUTPUT MODELS :



b. DUAL OUTPUT MODELS :



### NOTE:

a. C1=4.7µF / 100V, C2=10µF

b. C1 MUST BE ADDED WHEN APPLICATION .

c. C2 OPTIONAL TO MINIMIZE THE R & N < 100mV .

d. MAX. 80% LOAD WHEN INPUT VOLTAGE AT 9-11VDC FOR 9-36VDC INPUT MODELS & 18-21VDC FOR 18-72VDC INPUT MODELS .